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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/714,215	11/13/2003	Yuet-Ying Yu	END920030065US1 (IEN-10-5)	2551
26681	7590	03/10/2005	EXAMINER NGUYEN, VINH P	
DRIGGS, LUCAS BRUBAKER & HOGG CO. L.P.A. DEPT. IEN 8522 EAST AVENUE MENTOR, OH 44060			ART UNIT 2829	PAPER NUMBER

DATE MAILED: 03/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/714,215

Applicant(s)

YU ET AL.

Examiner

VINH P NGUYEN

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 23 November 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1,2,10,11,19 and 20 is/are rejected.
- 7) ☒ Claim(s) 3-9 and 12-18 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 13 November 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 1103.
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: \_\_\_\_\_.

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.
2. The abstract of the disclosure is objected to because legal phraseology such as “means” is used. Correction is required. See MPEP § 608.01(b).
3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-2 and 10-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Matrone (Pat # 4,352,061).

As to claims 1 and 10, Matrone discloses a test fixture as shown in figures 2- 3 having a multi-probe tester (14), with a plurality of test probes (30,47) arranged in a distribution pitch, an electrical circuit testing apparatus (13), a module (18) having a top surface comprising a first electrical device with a first plurality of contact pads (19) arrayed in the distribution pitch on a first device tip surface and means for masking (16) for masking at least one first test probe when the plurality of test probes are brought into contact with the first device tip surface to form a first electrical engagement. It is noted that the testing apparatus provides a circuit test run .

As to claims 2 and 11, it appears that the mask membrane (16) defining a first aperture (34) and disposed between the first device tip surface and the plurality of probes (30,47) wherein the first at least one test probe is aligned to contact the mask member (16) and a first remainder of the test probes (30,47) is aligned within the first aperture (34) thereby passing through the mask member (16). It is noted that the mask member prevents the first at least one test probe from making contact with the module top surface.

5. Claims 19-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Burr et al (Pat # 4,565,966).

As to claim 19, Burr et al disclose an apparatus for testing of electrical interconnection networks as shown in figure 1 having a control computer (40) for controlling a plurality of test probes (20,22) when a program inside the control computer is executed, the computer is able to select and disregard at least one test probe input from a test routine executed on the probes (20,22). It appears that the computer (40) inherently includes a memory (computer usable medium) for storing a computer readable program for controlling the test probes.

As to claim 20, it appears that the computer of Burr et al also cause to select and disregard a second probe (20) or the first probe (22).

6. Claims 3-9 and 12-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The prior art does not disclose multi probe tester having a tool application program configured to control the test probes and means for masking the test probes is at least one software loaded into the tool application program to cause an input from the first test probe to be disregarded during a test routine, in combination with other claimed elements.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Vaucher (pat # 5,216,358) disclose device for testing a printed circuit board.

Fjelstad (pat # 6,211,690) disclose an apparatus for electrically testing bare printed circuits.

Fohlich (Pat # 4,471,298) disclose an apparatus for automatically electrically testing printed circuit boards.


Look et al (Pat # 5,150,042) disclose on-wafer hall effect measurement system.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VINH P NGUYEN whose telephone number is (571)-272-1964. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications

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may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
VINH P. NGUYEN  
PRIMARY EXAMINER  
ART UNIT 2829  
03/03/05